

AMENDMENTS TO THE SPECIFICATIONIN THE SPECIFICATION:

Please amend the paragraph beginning on page 8, line 17, as follows:

—It is to be noted that in this invention, the comparator circuit 33 also may be implemented by an inverter. The inverter receives the first output voltage V_{FD} and determines the level of the output reset signal according to the magnitude of the first output voltage V_{FD} . When the power source is just started, the value of the first output voltage V_{FD} is smaller than a default threshold value of the inverter. At this time, the inverter regards the first output voltage V_{FD} as a low-level signal and inversely outputs a high-level signal to enable the reset signal Reset. Because the value of the first output voltage V_{FD} increases as time elapses, when the value of the first output voltage V_{FD} is greater than the default threshold value of the inverter, the inverter regards the first output voltage V_{FD} as a high level signal and inversely outputs a low-level signal to disable the reset signal Reset. In this embodiment, the working principle of the power-on reset circuit in FIG. 3 is illustrated in FIG. 6. —